Original code from Ron Kalin

A computer screen shot of a code

Description automatically generated

Netlist for the above code

A screenshot of a computer program

Description automatically generated

Using the Netlist to design the test bench (note no intermediate output has been monitored)

A screenshot of a computer program

Description automatically generated

RTL Simulation Output with Bit waveforms ( Note the output matches the expected values, since Excess3 = BCD + 4’b0011)

A screenshot of a computer

Description automatically generated